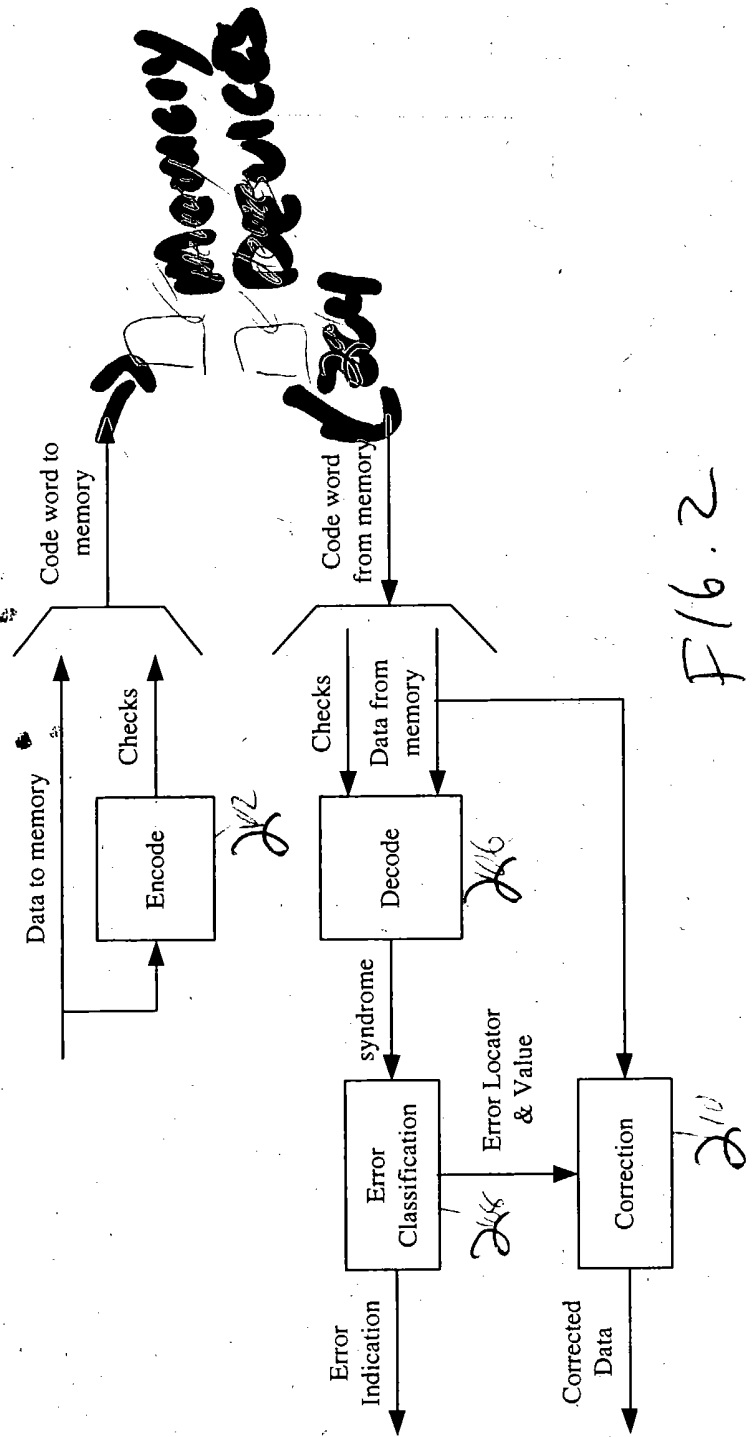


2 bytes of a codeword from memory, transferred in two clock phases.

2 bytes mapped to the inputs of the ECC block.

In the first clock phase n0 and n2 are transferred and mapped to the first nibble of each of two symbols of the



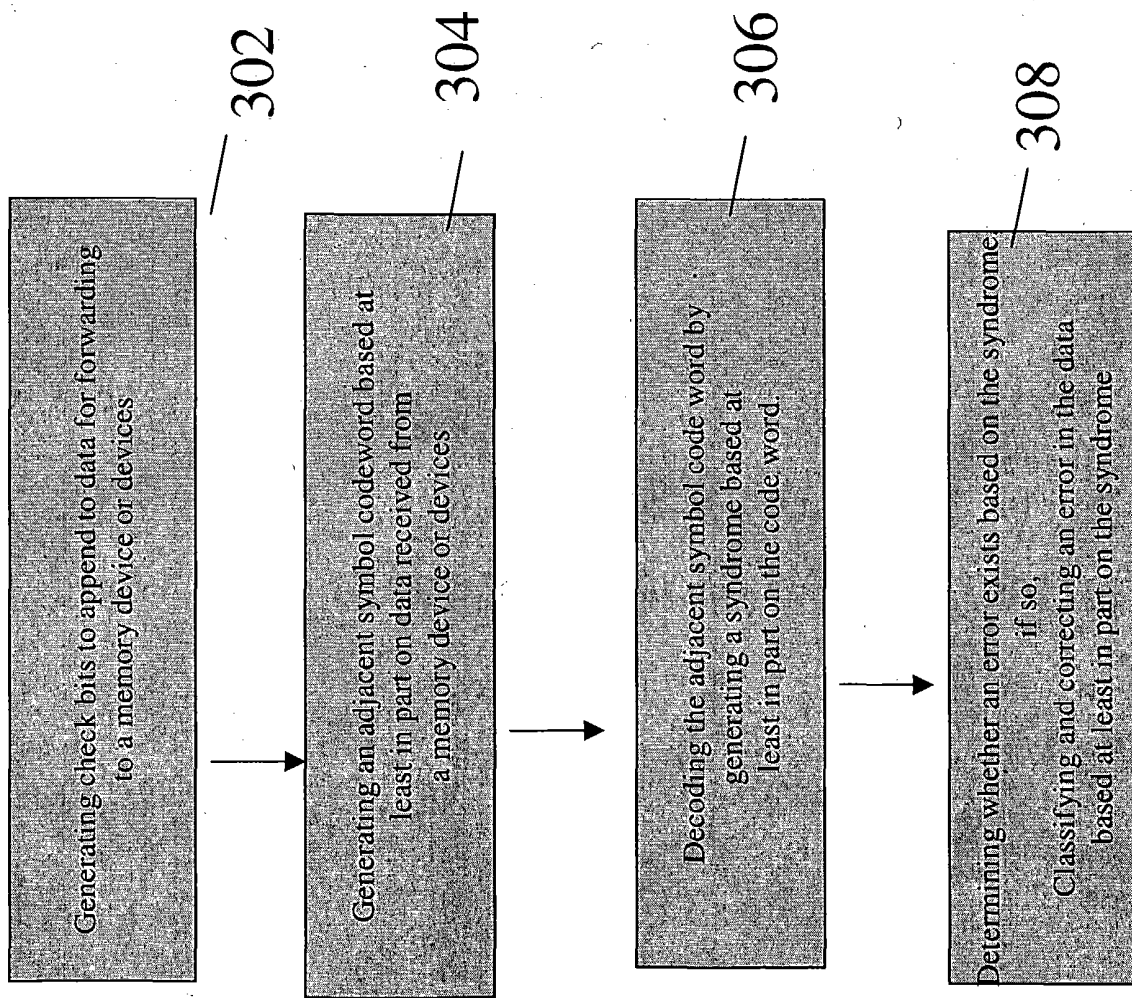
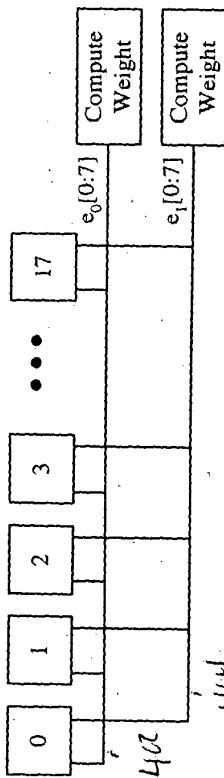


Figure 3

Adjacent pairs



~~Figure 4~~

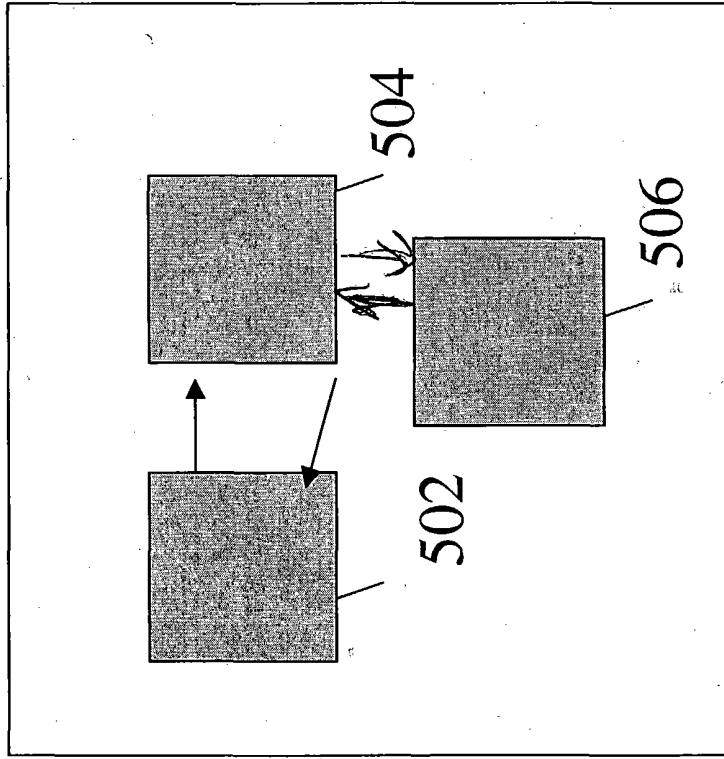


FIGURE 5